

Test fuer Mustertest(0123456)

Which pins can be used for external interrupts?

- Pin ADC1.
 - Pin PA0.
 - Pin PB1.
- JA NEIN

Which statements about the PORT register are correct?

- The register can control the pull-ups.
 - Read accesses to the register are ignored.
 - It selects whether the port pins are used for input or output.
- JA NEIN

You have a switch which is connected to GND and to INT0. Which bits/registers do you have to set in order to react to the button being pressed (but not to the release of the button)?

- ISC01
 - GICR
 - ISC10
- JA NEIN

Which registers are important when using external interrupts?

- Register TIMSK
 - Register GICR
 - Register GIFR
- JA NEIN

Which flags of the status register are influenced by the instruction "ANDI", and how?

- Bit S can be set or cleared.
 - Bit H can be set or cleared.
 - Bit V can be set or cleared.
- JA NEIN

Which events can wake the controller from the sleep mode "Idle"?

- Timer0 interrupt.
 - External reset.
 - External interrupt from INT1 on the rising edge.
- JA NEIN

Which instructions can access all registers R0-R31?

- | | | |
|-----------------------|-----------------------|-----|
| <input type="radio"/> | <input type="radio"/> | IN |
| <input type="radio"/> | <input type="radio"/> | LDI |
| <input type="radio"/> | <input type="radio"/> | LD |
| JA | NEIN | |

What does the instruction "LD R1, X" do?

- | | | |
|-----------------------|-----------------------|---|
| <input type="radio"/> | <input type="radio"/> | It performs a read access to the memory address specified in X. |
| <input type="radio"/> | <input type="radio"/> | It loads the contents of register X into R1. |
| <input type="radio"/> | <input type="radio"/> | It increments the X pointer. |
| JA | NEIN | |

Which statements about the shift-left and rotate-left operations are true?

- | | | |
|-----------------------|-----------------------|---|
| <input type="radio"/> | <input type="radio"/> | Both shift the msb into the carry flag. |
| <input type="radio"/> | <input type="radio"/> | Both shift the carry flag into the lsb. |
| <input type="radio"/> | <input type="radio"/> | Both shift the lsb into the carry flag. |
| JA | NEIN | |

You want to output a 1 on bit PB5. All other bits of the port should remain in their current states. Which of the following statements achieve that? (Note: The code is not necessarily executed right after the reset) (Note: the – separates two lines)

- | | | |
|-----------------------|-----------------------|--|
| <input type="radio"/> | <input type="radio"/> | out PORTB, (1 « 5) – out DDRB, (1 « 5) |
| <input type="radio"/> | <input type="radio"/> | ldi R16, 0x05 – out PORTB, R16 – out DDRB, R16 |
| <input type="radio"/> | <input type="radio"/> | in R16, PORTB – andi R16, 0x20 – out PORTB, R16 – (same with DDRB) |
| JA | NEIN | |